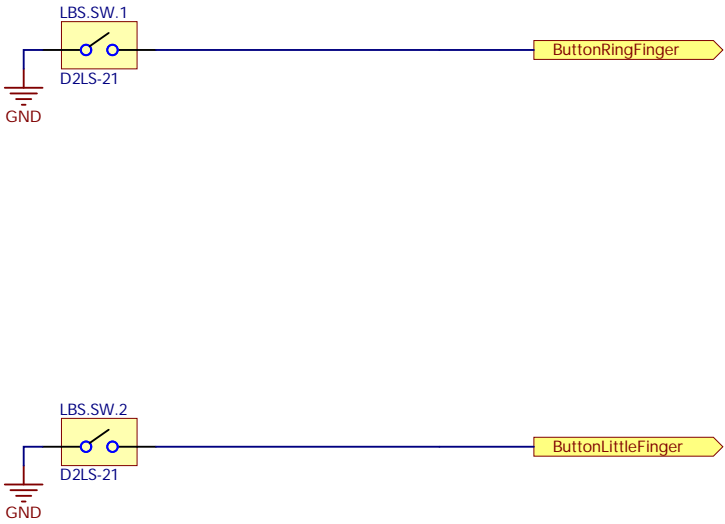


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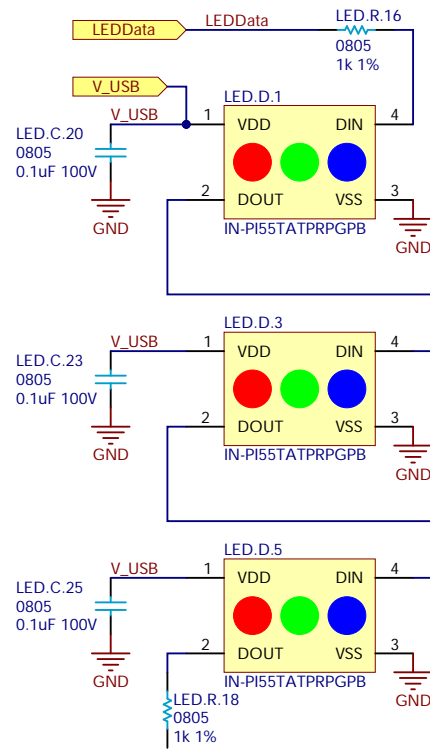
Please see the CERN OHL v.1.2 for applicable conditions.



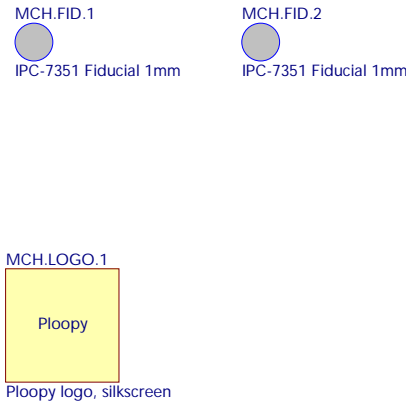
These two switches actuate the buttons for the ring and little fingers. They're located on the base board, unlike the switches for the rest of the controls.

1k resistors provide some ESD protection, limiting injection currents.

Since the pull-up resistors are about 40k on the GPIO, the caps on this circuit limit the rise time to about 1 millisecond, which should help with debouncing if populated.

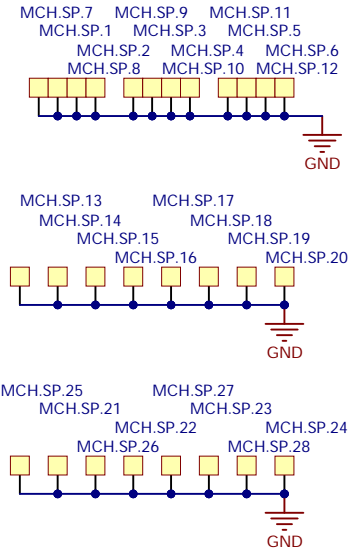


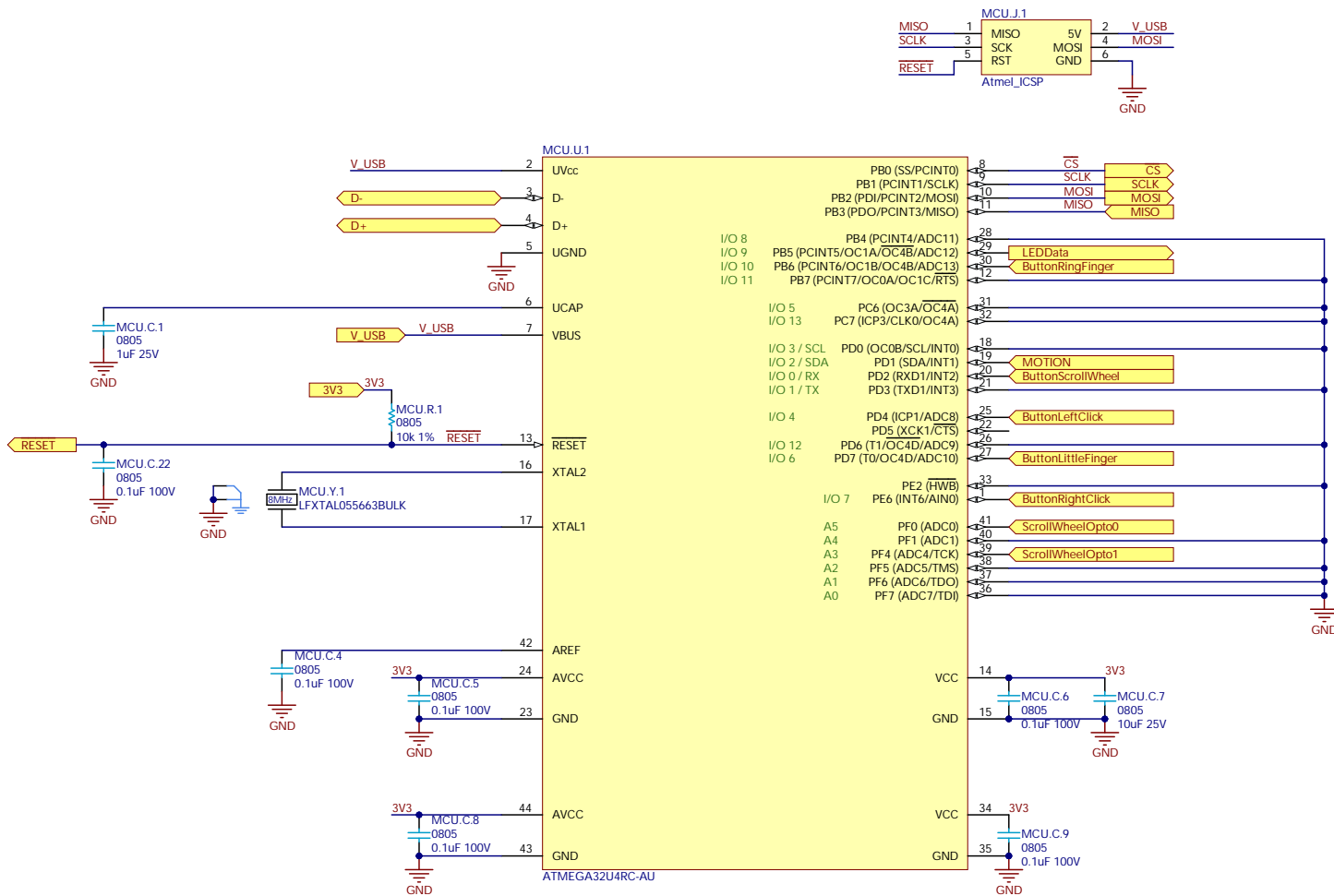
Pick and Place Fiducials

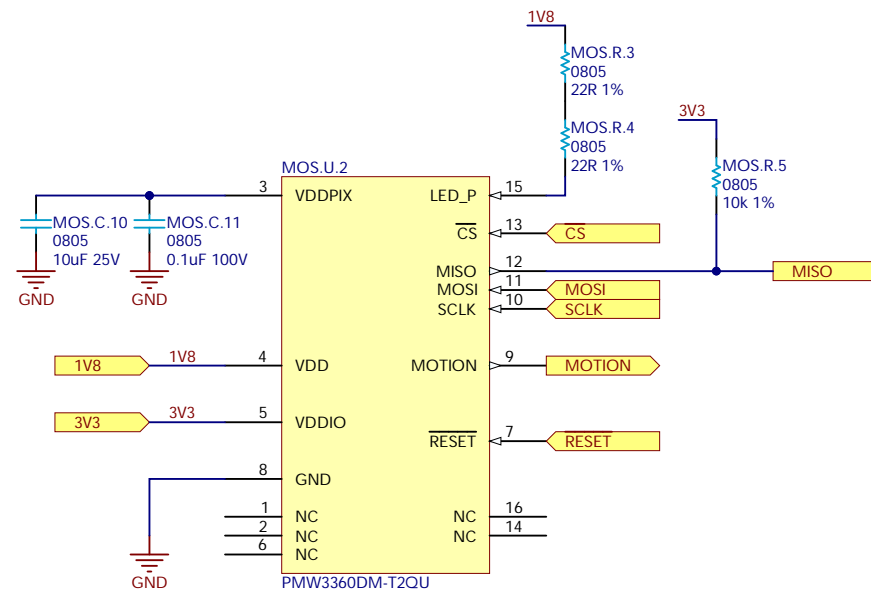


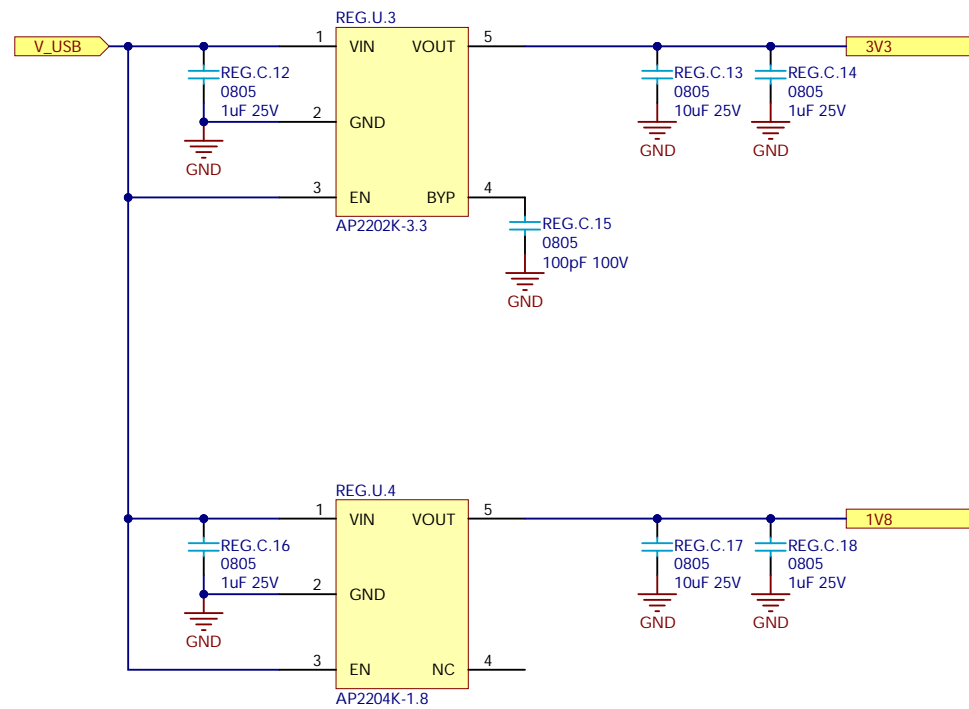
Spark Gaps -- Case

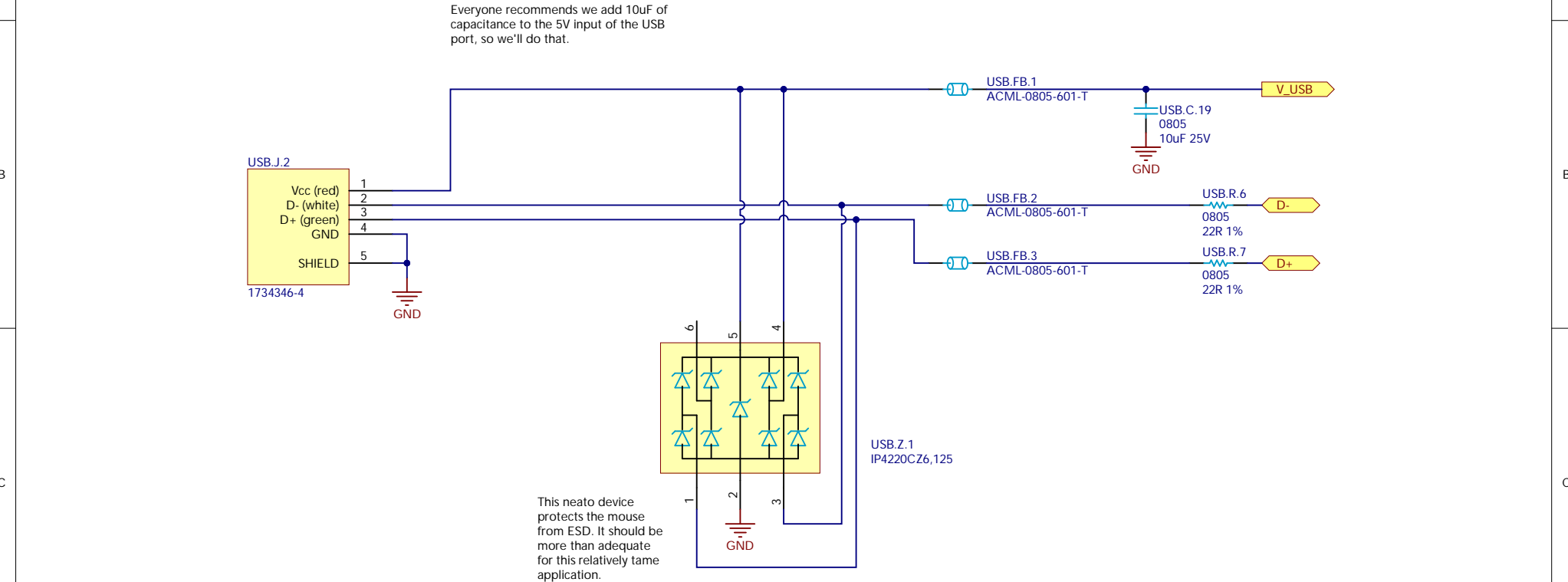
Since the case has gaps in it, we expect ESD to worm its way in via creepage and perhaps other ways. To protect the board from this eventuality, we place spark gaps along the edges.













The two PCBs have castellated connections, which are represented here. These carry a shared ground and all the signals from the various UI bits and pieces that are hosted on the vertical board.

The inline resistors on the switch lines provide a modicum of ESD protection, limiting injected currents. Since the case should provide some decent protection against static discharge, we settle for a pretty basic solution.

